

10/4/11, 067
Patent Handout

① ③2 ⑥4 ⑨6

— Examiner Notes —

① Testing System for Verifying Proper Functionality of DUT
Method of describing a set of tests capable of being performed on DUT

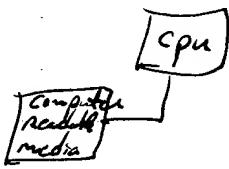
4601032

Spec
Inventor

5,995,915
6,195,616

11/12 2nd does not perform claimed function

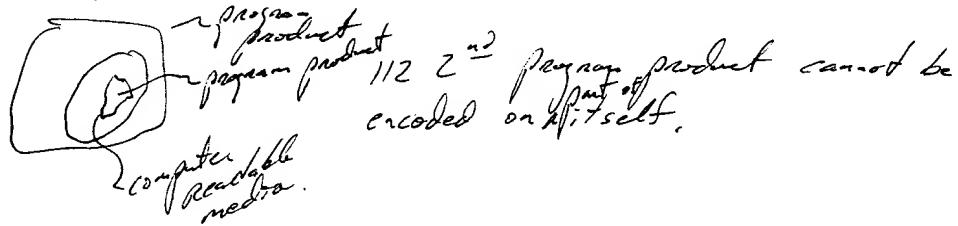
③2 Computer Sys



computer code for describing a set of test performance
configured to cause cpu to ID scenario space of DUT

⑥4 Computer Program Product for describing set of test performance on DUT

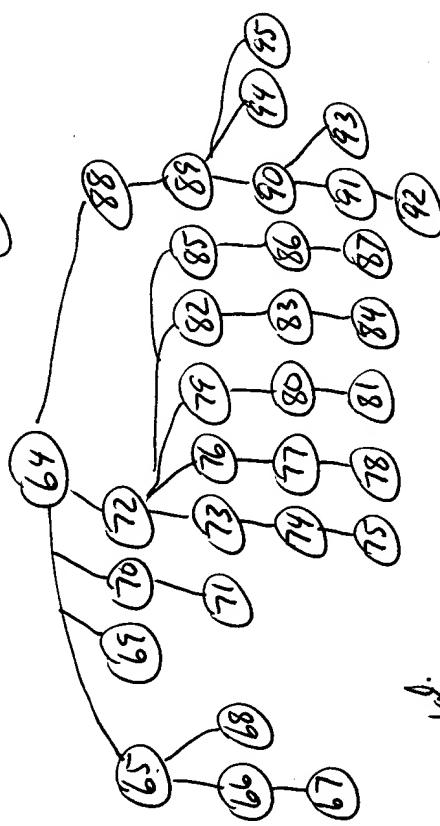
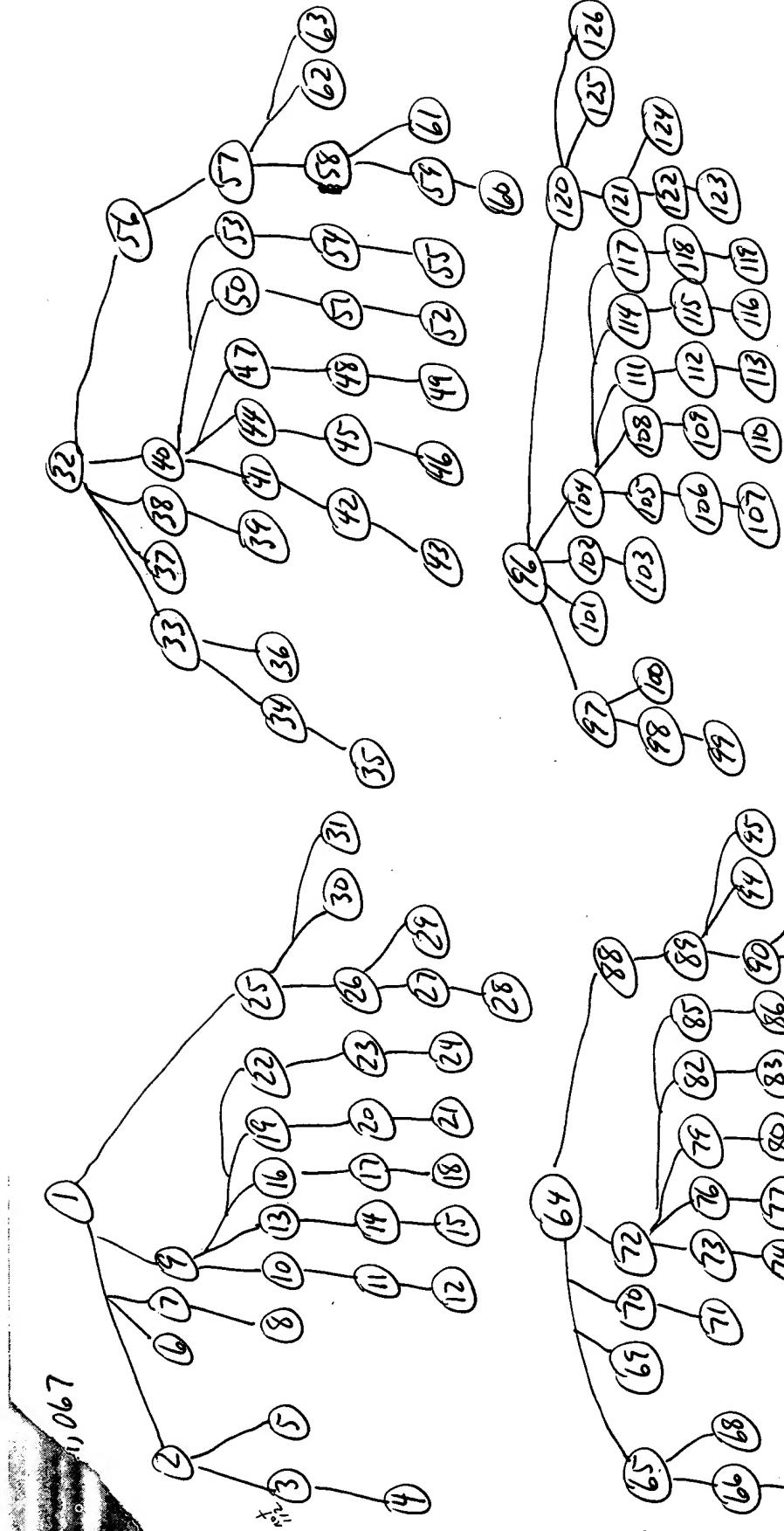
1st set of instru configured to ID scenario space of DUT
Computer readable media where Program Product on media



⑨6 Apparatus configured to describe set of tests performable on DUT
Means for ID a scenario space of DUT

Apparatus
No TDS

7/28/03
4/9/03 Chas. OK



Oct 1915
5, 6, 7, 10, 11, 15, 17

8 | 503,428
8 | 500,826 - 6,195,915
8 | 100,400 5,195,915

Adnan Hamid

10/15 73.00